

Set	Items	Description
S1	95627	LU OR PU OR (PHYSICAL OR LOGICAL) () UNIT? ? OR NODE? OR NET-WORK () DEVICE? OR SUBNODE?
S2	273500	CHILD? OR SUBORDINAT? OR BRANCH? OR TWIG? ? OR SUBDIRECTOR?
S3	548970	NAME? OR NAMING OR LABEL? OR IDENTIF? OR ID OR IDS
S4	1796537	COUNTER? OR NUMBER? OR INCREMENT? OR DECREMENT? OR NUMERAT?
S5	1028755	TRUNCAT? OR STEM OR STEMS OR STEMMING OR CHOP? OR CUT OR CUTS OR CUTTING OR ABBREVIAT?
S6	111	S1 AND S2 AND S3 AND S4
S7	4	S5 AND S6
S8	978	S2 (2N) S3
S9	60	S1 AND S8
S10	23	S9 AND (S5 OR S4)
S11	22	S10 NOT S7
S12	2	S11 AND IC=G06F-015?
S13	20	S11 NOT S12
S14	432	S3 (4N) S4 (5N) S5
S15	12	S14 AND (S2 OR PARENT? OR S1)
S16	12	S15 NOT (S10 OR S11)
S17	31	S2 (4N) S4 (5N) (TRUNCAT? OR STEM? OR ABBREVIAT?)
S18	2	S1 AND S17
S19	2	S18 NOT S16
S20	1	S17 AND IC=G06F?
S21	196	S3 AND S4 AND S5 (N) (TERM? OR WORD? OR LABEL? OR ID OR IDENTIFIER? OR NAME?)
S22	1931531	COMBIN? OR JOIN? OR ADD OR APPEND? OR ADDING
S23	23	S21 AND S22
S24	23	S23 NOT (S15 OR S17 OR S16)
S25	3	S24 AND IC=(G06F? OR H04L?)
S26	20	S24 NOT S25
S27	4	S1 AND S2 AND (NAMING OR LABELING OR (ASSIGN?) () (NAME? OR - LABEL? OR ID OR IDS OR IDENTIFIER?))

File 347:JAPIO Nov 1976-2003/Dec(Updated 040402)

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File 350:Derwent WPIX 1963-2004/UD,UM &UP=200423

(c) 2004 Thomson Derwent

13/5/18 (Item 14 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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009587417 **Image available**
WPI Acc No: 1993-280963/199335
Related WPI Acc No: 1990-079358
XRPX Acc No: N93-215852

**System connecting multiple devices to master small computer system
interface bus - enables communication between host with first SCSI and
multiple target devices**

Patent Assignee: HEWLETT-PACKARD CO (HEWP)
Inventor: LARNER J B
Number of Countries: 001 Number of Patents: 001
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 5239632	A	19930824	US 92869975	A	19920416	199335 B

Priority Applications (No Type Date): US 92869975 A 19920416

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
US 5239632	A	17	G06F-001/00	

Abstract (Basic): US 5239632 A

The system includes a master SCSI bus connected to the first SCSI port of the host, which has an associated ID **number** used for identification on the master SCSI bus. A subordinate SCSI bus is connected to the SCSI ports of the multiple target devices. A minnow device provides second and third SCSI ports, for transferring the communications between the host and the targets selected by the host. The minnow is connected to the master and subordinate SCSI buses at the second and third SCSI ports respectively.

The minnow device has first and second master bus SCSI ID **numbers** used to identify the minnow on the subordinate and master SCSI buses respectively, and for converting a SCSI **logical unit number** received from the host device to a second **subordinate bus SCSI ID number**. The second **subordinate bus SCSI ID number** identifies the selected target device on the subordinate SCSI bus to establish communications between the host device and the selected target device.

USE/ADVANTAGE - Sets master SCSI ID for minnow device. Performs reselect to allow communication with target.

Dwg.3/8

Title Terms: SYSTEM; CONNECT; MULTIPLE; DEVICE; MASTER; COMPUTER; SYSTEM;
INTERFACE; BUS; ENABLE; COMMUNICATE; HOST; FIRST; MULTIPLE; TARGET;
DEVICE

Derwent Class: T01

International Patent Class (Main): G06F-001/00

File Segment: EPI

13/5/1 (Item 1 from file: 347)
DIALOG(R)File 347:JAPIO
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07382913 **Image available**
DATA CONFIGURATION METHOD OF DIRECTORY TYPE RETRIEVING ENGINE

PUB. NO.: 2002-251413 [JP 2002251413 A]
PUBLISHED: September 06, 2002 (20020906)
INVENTOR(s): MURAMATSU SHIGEKI
TAKAGI SATORU
APPLICANT(s): KDDI CORP
APPL. NO.: 2001-046611 [JP 200146611]
FILED: February 22, 2001 (20010222)
INTL CLASS: G06F-017/30

ABSTRACT

PROBLEM TO BE SOLVED: To provide a data configuration method of a directory type retrieving engine that dynamically changes the priority of a retrieving result or notifies, or does not notify, a retrieving result based on the status of actual retrieves by many searchers.

SOLUTION: A subordination degree value is set for each link between a parent **node** and a child **node**. The parent **node** notifies the child **nodes** of retrieving results in descending order of the subordinate degree value included in a child **node identifier**. The **number** of accesses from the parent **node** to each child **node** is retrieved for each liked, and the subordinate degree value is increased or decreased based on the ratio of the **number** of accesses to the child **node** to the total **number** of accesses of the parent **node**. Conversely, the subordinate degree value is increased or decreased based on the ratio of the **number** of accesses from the parent **node** to the child **node** to the total **number** of accesses to the child **node**.

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13/5/6 (Item 2 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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015923190 **Image available**
WPI Acc No: 2004-081030/200408
Related WPI Acc No: 2004-155791
XRPX Acc No: N04-064730

Network architecture for industrial actuator, identifies children nodes in specific layer, based on addresses allocated for nodes in that layer and their decedent nodes , and maximum permissible number of nodes in that layer

Patent Assignee: ALLEN V A (ALLE-I); ANDRIC O (ANDR-I); CHEN P (CHEN-I);
HESTER L E (HEST-I); HUANG Y (HUAN-I); MOTOROLA INC (MOTI)
Inventor: ALLEN V A; ANDRIC O; CHEN P; HESTER L E; HUANG Y
Number of Countries: 103 Number of Patents: 002
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 20030227931	A1	20031211	US 2002386511	P	20020606	200408 B
			US 2002304428	A	20021126	
WO 2003105502	A1	20031218	WO 2003US17929	A	20030605	200409

Priority Applications (No Type Date): US 2002386511 P 20020606; US
2002304428 A 20021126

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
US 20030227931	A1	16	H04L-012/28	Provisional application US 2002386511

WO 2003105502 A1 E H04Q-007/20

Designated States (National): AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA
CH CN CO CR CU CZ DE DK DM DZ EC EE ES FI GB GD GE GH HR HU ID IL IN
IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NI NO
NZ OM PH PL PT RO RU SC SD SE SG SK SL TJ TM TN TR TT TZ UA UG UZ VC VN
YU ZA ZM ZW

Designated States (Regional): AT BE BG CH CY CZ DE DK EA EE ES FI FR GB
GH GM GR HU IE IT KE LS LU MC MW MZ NL OA PT RO SD SE SI SK SL SZ TR TZ
UG ZM ZW

Abstract (Basic): US 20030227931 A1

NOVELTY - A layer comprising network **nodes** , is said to be Kth layer, when there are 'K' **number** of hops between **nodes** in the kth layer and the root **node** . The addresses of the children **nodes** in kth layer, are assigned by skipping a **number** (Cskip) which is equal to the ratio of the sum of addresses allocated for **nodes** in kth layer and decedent **nodes** of Kth layer, to the maximum permissible **number** of **nodes** in kth layer.

DETAILED DESCRIPTION - INDEPENDENT CLAIMS are included for the following:

- (1) children **nodes** address assigning method; and
- (2) messages routing method; and
- (3) network addresses assigning method.

USE - Wireless network architecture used in wireless personal computer peripherals, toy, security device, wireless sensor and actuator, control system.

ADVANTAGE - Messages are routed efficiently, by network using specific method to **identify** the **children nodes** .

DESCRIPTION OF DRAWING(S) - The figure shows the network topology.
network (100)
network **nodes** (102,106)
clusters (108,110,114,116,118,120)
pp; 16 DwgNo 1/8

Title Terms: NETWORK; ARCHITECTURE; INDUSTRIAL; ACTUATE; IDENTIFY; CHILD;
NODE ; SPECIFIC; LAYER; BASED; ADDRESS; ALLOCATE; **NODE** ; LAYER; **NODE** ;
MAXIMUM; PERMIT; **NUMBER** ; **NODE** ; LAYER

Derwent Class: T01; W01

International Patent Class (Main): H04L-012/28; H04Q-007/20

International Patent Class (Additional): H04L-012/56

File Segment: EPI

13/5/15 (Item 11 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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010544847 **Image available**
WPI Acc No: 1996-041800/199605
XRPX Acc No: N96-035045

Query optimisation method using predicates applicable query attributes - involves making graph of query and moving predicates around parent and child nodes in graph so that they will be applied early in optimised query

Patent Assignee: AT & T CORP (AMTT); AMERICAN TELEPHONE & TELEGRAPH CO (AMTT); LUCENT TECHNOLOGIES INC (LUCE)

Inventor: LEVY A Y; MUMICK I S

Number of Countries: 006 Number of Patents: 004

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
EP 689148	A1	19951227	EP 95303634	A	19950530	199605 B
JP 7334529	A	19951222	JP 95161573	A	19950606	199609
CA 2147678	A	19951207	CA 2147678	A	19950424	199614
US 5659725	A	19970819	US 94254215	A	19940606	199739

Priority Applications (No Type Date): US 94254215 A 19940606

Cited Patents: 5.Jnl.Ref

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
EP 689148	A1	E	17		

Designated States (Regional): DE FR GB

JP 7334529	A	15
US 5659725	A	11

Abstract (Basic): EP 689148 A

The method involves making a query graph for the query in which a **node** of the graph has a label with one predicate applicable to the attributes in the **node**. In a parent **node** in the graph, a new predicate is inferred in the parent **node**'s label from a predicate in a label belonging to any child **node** of the parent.

In a child **node** of the graph, a second new predicate is inferred in the **child node**'s **label** from a predicate in a label belonging to any parent of the **node**. An optimised query from the query graph is generated. The **number** of predicates in the query graph is reduced. Any predicate from a given **node**'s label which is applied at a descendant of the given **node** is removed, together with any predicate in a label which is implied by another predicate in the label.

ADVANTAGE - Optimisation is not dependent on join order and works where **nodes** of graph cannot be merged. Optimisation applied as early as possible in computation.

Dwg.5/6

Title Terms: QUERY; OPTIMUM; METHOD; APPLY; QUERY; ATTRIBUTE; GRAPH; QUERY; MOVE; PARENT; CHILD; **NODE** ; GRAPH; SO; APPLY; EARLY; OPTIMUM; QUERY

Derwent Class: T01

International Patent Class (Main): G06F-017/30

International Patent Class (Additional): G06F-007/10; G06F-012/00

File Segment: EPI

13/5/2 (Item 2 from file: 347)
DIALOG(R)File 347:JAPIO
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05957641 **Image available**
MANAGING METHOD FOR TREE STRUCTURE TYPE DATA

PUB. NO.: 10-240741 [JP 10240741 A]
PUBLISHED: September 11, 1998 (19980911)
INVENTOR(s): KONISHI FUMIKAZU
KOBAYASHI NOBUYUKI
NATSUME YOSHIHISA
NISHIOKA SHUICHI
APPLICANT(s): NIPPON TELEGR & TELEPH CORP <NTT> [000422] (A Japanese
Company or Corporation), JP (Japan)
APPL. NO.: 09-046368 [JP 9746368]
FILED: February 28, 1997 (19970228)
INTL CLASS: [6] G06F-017/30
JAPIO CLASS: 45.4 (INFORMATION PROCESSING -- Computer Applications)

ABSTRACT

PROBLEM TO BE SOLVED: To directly obtain the position of a physical record by using an **ID** when a **child** of an arbitrary **node** are retrieved by representing the parent-child relation of a tree structure by using IDs representing the storage positions where respective **nodes** are stored.

SOLUTION: To store tree structure type data 100 in a relational data base, the **number** of branches between the parent and children is fixed to a finite **number** and the relation between the parent and children is deformed into a structure 101. Then data corresponding to **nodes** 1, 2, and 3 of the tree structure are stored as records 103, 104, and 107 of the relational data base in a table 102, and IDs 1100, 1200, and 1300 indicating the storage positions of the stored records are provided and stored in columns 105, 108, and 110 of the records. Consequently, one **node** requires up to three branches (up arrow, right array, and left array) like a **node** 6 and the **number** of the branches is fixed. Consequently, fast retrieval of the tree structure data becomes possible.

Set	Items	Description
S1	225364	LU OR PU OR (PHYSICAL OR LOGICAL)()UNIT? ? OR NODE? OR NET-WORK()DEVICE? OR SUBNODE?
S2	255294	CHILD? OR SUBORDINAT? OR BRANCH? OR TWIG? ? OR SUBDIRECTOR?
S3	709092	NAME? OR NAMING OR LABEL? OR IDENTIF? OR ID OR IDS
S4	979216	COUNTER? OR NUMBER? OR INCREMENT? OR DECREMENT? OR NUMERAT?
S5	472587	TRUNCAT? OR STEM OR STEMS OR STEMMING OR CHOP? OR CUT OR CUTS OR CUTTING OR ABBREVIAT?
S6	505	S1 (12N) S2 (12N) S3 (12N) S4
S7	9	S5 (12N) S6
S8	3182	S2(2N)S3
S9	296	S1 (12N) S8
S10	34	S9 (12N) (S5 OR S4)
S11	32	S10 NOT S7
S12	1	S11 AND IC=G06F-015?
S13	31	S11 NOT S12
S14	921	S3(4N)S4(5N)S5
S15	33	S14 (12N) (S2 OR PARENT? OR S1)
S16	31	S15 NOT (S10 OR S11)
S17	80	S2(4N)S4(5N)(TRUNCAT? OR STEM? OR ABBREVIAT?)
S18	4	S1 (10N) S17
S19	4	S18 NOT S16
S20	6	S17 AND IC=G06F?
S21	4293	S3 AND S4 AND S5(N) (TERM? OR WORD? OR LABEL? OR ID OR IDENTIFIER? OR NAME?)
S22	1132013	COMBIN? OR JOIN? OR ADD OR APPEND? OR ADDING
S23	2013	S3(12N)S4(12N)S5(N) (TERM OR TERMS OR WORD OR WORDS OR LABEL OR LABELS OR ID OR IDS OR IDENTIFIER OR IDENTIFIERS OR NAME - OR NAMES)
S24	288	S22(S)S23
S25	45	S6 AND IC=G06F-015?
S26	41	S7 OR S10 OR S11
S27	71	(S25 OR S26 OR S20 OR S18 OR S19 OR S16) AND IC=G06F?
S28	5	S27 AND IC=G06F-015/173
S29	47	S26 OR S20
S30	2	S29 AND IC=G06F-015?
S31	7	S28 OR S30
S32	9	(LUN OR LUNS) (4N) (NAMING OR LABELING OR ADDRESSING OR ASSIGN?() (ID OR IDENTIFIER?))
S33	119552	S3(10N)S4
S34	7	S32 AND S33

File 348:EUROPEAN PATENTS 1978-2004/Apr W01
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File 349:PCT FULLTEXT 1979-2002/UB=20040408,UT=20040401
(c) 2004 WIPO/Univentio

34/5,K/6 (Item 4 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
(c) 2004 WIPO/Univentio. All rts. reserv.

00762383 **Image available**

**SYSTEM AND METHOD FOR MAINTAINING CACHE COHERENCY AND DATA SYNCHRONIZATION
IN A COMPUTER SYSTEM HAVING MULTIPLE ACTIVE CONTROLLERS
SYSTEME ET PROCEDE PERMETTANT DE MAINTENIR LA COHERENCE DE MEMOIRE CACHE ET
LA SYNCHRONISATION DE DONNEES DANS UN SYSTEME INFORMATIQUE DOTE DE
DISPOSITIFS DE COMMANDE ACTIFS MULTIPLES**

Patent Applicant/Assignee:

MYLEX CORPORATION, 34551 Ardenwood Boulevard, Fremont, CA 94555-3607, US,
US (Residence), US (Nationality)

Inventor(s):

SKAZINSKI Joseph, 207 Cheyenne Drive, Bertoud, CO 80513, US
MCKEAN Brian, 2818 Humboldt Circle, Longmont, CO 80503, US
OTTERNESS Noel S, 3827 Paseo del Prado, Boulder, CO 80301, US

Legal Representative:

ANANIAN R Michael, Flehr Hohbach Test Albritton & Herbert LLP, 4
Embarcadero Center, Suite 3400, San Francisco, CA 94111-4187, US

Patent and Priority Information (Country, Number, Date):

Patent: WO 200075786 A1 20001214 (WO 0075786)
Application: WO 2000US12282 20000505 (PCT/WO US0012282)
Priority Application: US 99325033 19990603

Designated States: AE AG AL AM AT AU AZ BA BB BG BR BY CA CH CN CR CU CZ DE
DK DM DZ EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC
LK LR LS LT LU LV MA MD MG MK MN MW MX NO NZ PL PT RO RU SD SE SG SI SK
SL TJ TM TR TT TZ UA UG UZ VN YU ZA ZW
(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE
(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG
(AP) GH GM KE LS MW SD SL SZ TZ UG ZW
(EA) AM AZ BY KG KZ MD RU TJ TM

Main International Patent Class: G06F-012/08

Publication Language: English

Filing Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 17055

English Abstract

System, method and computer program for maintaining cache coherency amongst a plurality of caching storage controllers (28, 29) operating in unison for supplying data in response to requests from one or more host computers. The method comprises the steps of defining a reservation data structure to maintain reserved, partial, and full ownership status of data extents that are part of the logical unit or storage volume, and using the reservation data structure to verify that a new update to the data is allowed (203-215).

French Abstract

Système, procédé et programme informatique permettant de maintenir la cohérence de mémoire cache parmi une pluralité de dispositifs de commande (28, 29) de mise en mémoire dans des mémoires cache, qui fonctionnent à l'unisson, pour fournir des données en réponse à des demandes provenant d'un ou plusieurs ordinateurs hôtes. Ledit procédé consiste à définir une structure de données de réservation pour maintenir le statut de propriété réservée, partielle ou entière des extensions de données qui font partie de l'unité logique ou du volume de stockage, et à utiliser la structure de données de réservation pour vérifier qu'une nouvelle mise à jour des données est permise (203-215).

Legal Status (Type, Date, Text)

Publication 20001214 A1 With international search report.

Publication 20001214 A1 With amended claims.

Examination 20010412 Request for preliminary examination prior to end of
19th month from priority date

Fulltext Availability:
Detailed Description

Detailed Description

... storage medium addressing
schememaybeused. InanSCSIbasedRAIDstoragesystem.theRAIDcontrollers
handle all data accesses from the host based on **LUN addressing** . By
using a **LUN** address value, the host computer can store data for given
data extent. Each LUN includes...Host Read Command (step 203). The Host
Read Command is directed a
Logical Unit (LU), **identified** by a Logical Unit **Number** (LUN), on the
controller.

The controller contains an internal mapping of which LU`N represents...
Host Write Command (step 233). The Host Write Command is directed a
Logical Unit (LU), **identified** by a Logical Unit **Number** (LUN), on the
controller.

- 25 The controller contains an internal mapping of which LUN represents
...a request for access to an extent occurs simultaneously through both
controllers, the lower controller **ID number** wins. A lock is an
ActiveLock the owning controller has the ability to perform operations...

Set	Items	Description
S1	16	INCREMENT?(2N)GLOBAL()COUNTER?
S2	16	INCREMENT?(2N)GLOBAL()COUNTER?
S3	8	S1 AND (SUBSTITUTE? OR SWITCH? OR SWAP? OR REPLACE?)
File 350:		Derwent WPIX 1963-2004/UD,UM &UP=200424 (c) 2004 Thomson Derwent
File 349:		PCT FULLTEXT 1979-2002/UB=20040408,UT=20040401 (c) 2004 WIPO/Univentio
File 348:		EUROPEAN PATENTS 1978-2004/Apr W01 (c) 2004 European Patent Office
File 275:		Gale Group Computer DB(TM) 1983-2004/Apr 16 (c) 2004 The Gale Group
File 148:		Gale Group Trade & Industry DB 1976-2004/Apr 16 (c)2004 The Gale Group

3/5,K/1 (Item 1 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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00931246 **Image available**

AN OPTIMIZED DYNAMIC BYTECODE INTERPRETER
INTERPRETEUR DE CODES D'OCTETS DYNAMIQUES OPTIMISES

Patent Applicant/Assignee:

TRIMEDIA TECHNOLOGIES INC, 1840 McCarthy Blvd., Milpitas, CA 95035, US,
US (Residence), US (Nationality)

Inventor(s):

VANDERSPEK Julius, 1185 Happy Hollow Avenue, San Jose, CA 95129, US,

Legal Representative:

HEAL Elaine M (et al) (agent), Penwick & West LLP, Two Palo Alto Square,
Palo, Alto Square, Palo Alto, CA 94306, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200265284 A1 20020822 (WO 0265284)

Application: WO 2002US3716 20020208 (PCT/WO US0203716)

Priority Application: US 2001782344 20010212

Designated States: AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU
CZ DE (utility model) DE DK DM DZ EC EE ES FI GB GD GE GH GM HR HU ID IL
IN IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO
NZ OM PL PT RO RU SD SE SG SI SK SL TJ TM TR TT TZ UA UG UZ VN YU ZA ZW
(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR
(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG
(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZM ZW
(EA) AM AZ BY KG KZ MD RU TJ TM

Main International Patent Class: G06F-009/45

Publication Language: English

Filing Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 2790

English Abstract

The present invention relates to bytecode interpretation. The inerpreter selects frequently executed bytecodes and translates them into corresponding machine code. The interpreter also extends a jump table (40) used by the interpreter to index the bytecodes with the machine code (44). The extension includes a reference to the frequently executed bytecodes as well as the corresponding machine code. Thus interpretation is dynamically profiled and optimized.

French Abstract

L'invention porte sur l'interpretation des codes d'octets. L'interpreteur selectionne les codes d'octets frequemment executes et les traduit en codes machine correspondants. L'interpreteur etend par ailleurs la table de recherche (40) qu'il utilise pour faire correspondre les codes d'octets avec les codes machine (44). L'extension comporte une reference aux codes d'octets frequemment executes et aux codes machine correspondants. L'interpretation est ainsi profilee et optimisee dynamiquement.

Legal Status (Type, Date, Text)

Publication 20020822 A1 With international search report.

Publication 20020822 A1 Before the expiration of the time limit for
amending the claims and to be republished in the
event of the receipt of amendments.

Examination 20021114 Request for preliminary examination prior to end of
19th month from priority date

Correction 20031106 Corrections of entry in Section 1: Due to a
technical problem at the time of international
publication, some information was missing (81). The
missing information now appears in the corrected
version.

Republication 20031106 A1 With international search report.

Fulltext Availability:
Detailed Description
Claims

Detailed Description

... One counter, JCOUNT [in], increments when a branch target is executed within method in. The **global counter**, 2o WCOUNT, **increments** when a branch target is executed anywhere in the entire program. A method that executes...its corresponding machine code location 74. The compiler/optimizer also updates the bytecode program to **replace** the first bytecode in the trace

6

in the trace 80.

The compiler/optimizer determines...

Claim

... of claim 23, wherein a first bytecode in the sequence of frequently executed bytecodes is **replaced** by the inserted entry in the bytecode table.

26 The virtual machine of claim 23...

3/5,K/5 (Item 1 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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01319481

Method for synchronizing databases stored on portable devices

Verfahren zum Synchronisieren von auf tragbaren Gerat gespeicherten
Datenbanken

Procede pour synchroniser des bases de donnees stockees dans des
dispositifs portables

PATENT ASSIGNEE:

SIEMENS AKTIENGESELLSCHAFT, (200520), Wittelsbacherplatz 2, 80333 Munchen
, (DE), (Applicant designated States: all)

INVENTOR:

Nikitin, Mikahail, Falstergade 8-B, 7, 9000 Aalborg, (DK)

Hansen, Martin, Enggaardsgade 68, 9000 Aalborg, (DK)

PATENT (CC, No, Kind, Date): EP 1128279 A1 010829 (Basic)

APPLICATION (CC, No, Date): EP 2000103954 000225;

DESIGNATED STATES: AT; BE; CH; CY; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI;

LU; MC; NL; PT; SE

EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI

INTERNATIONAL PATENT CLASS: G06F-017/30

ABSTRACT EP 1128279 A1

Method for synchronizing a first database stored on a first client
device with a second database stored on a second client device by sending
entries of the first database from the first client device to the second
client device or a server device, wherein a determination is made as to
whether an entry needs to be synchronized before sending off the entry
from the first client device.

ABSTRACT WORD COUNT: 69

NOTE:

Figure number on first page: 7

LEGAL STATUS (Type, Pub Date, Kind, Text):

Application: 010829 A1 Published application with search report

Withdrawal: 021218 A1 Date application deemed withdrawn: 20020301

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	200135	923
SPEC A	(English)	200135	2819
Total word count - document A			3742
Total word count - document B			0
Total word count - documents A + B			3742

...SPECIFICATION In step 41 the global counter GC is read. In a next step
42 the **global counter** is **incremented** by 1. Step 42 is followed by a
decision block 43 for deciding whether ENTRYi...

...Furthermore ENTRYi))cl) is transferred from client device 20 to the
other client devices to **replace** the corresponding ENTRYi)) in their
object stores. Alternatively transferring and **replacement** of the
corresponding entries ENTRYi)) is just requested.

The invention is advantageous over the known...

Set	Items	Description
S1	13049	(INCREASE? OR INCREMENT? OR RAISE?) (3N) (COUNTER?)
S2	131	S1 (10N) (SUBSTITUTE? OR SWITCH? OR APPEND? OR SWAP? OR REPLACE?)
S3	8	S2(S) (NAME? OR NAMING OR ID OR IDENTIFIER? OR ADDRESS? OR LABEL? OR TAG OR TAGS OR SUFFIX?)
S4	7	RD (unique items)
S5	6	S4 NOT PY>2000
File 275:	Gale Group Computer DB(TM) 1983-2004/Apr 16 (c) 2004 The Gale Group	
File 47:	Gale Group Magazine DB(TM) 1959-2004/Apr 16 (c) 2004 The Gale group	
File 636:	Gale Group Newsletter DB(TM) 1987-2004/Apr 16 (c) 2004 The Gale Group	
File 16:	Gale Group PROMT(R) 1990-2004/Apr 15 (c) 2004 The Gale Group	
File 624:	McGraw-Hill Publications 1985-2004/Apr 14 (c) 2004 McGraw-Hill Co. Inc	
File 484:	Periodical Abs Plustext 1986-2004/Apr W2 (c) 2004 ProQuest	
File 813:	PR Newswire 1987-1999/Apr 30 (c) 1999 PR Newswire Association Inc	
File 141:	Readers Guide 1983-2004/Apr (c) 2004 The HW Wilson Co	
File 696:	DIALOG Telecom. Newsletters 1995-2004/Apr 15 (c) 2004 The Dialog Corp.	
File 553:	Wilson Bus. Abs. FullText 1982-2004/Apr (c) 2004 The HW Wilson Co	
File 621:	Gale Group New Prod. Annou. (R) 1985-2004/Apr 16 (c) 2004 The Gale Group	
File 674:	Computer News Fulltext 1989-2004/Apr W1 (c) 2004 IDG Communications	
File 160:	Gale Group PROMT(R) 1972-1989 (c) 1999 The Gale Group	
File 15:	ABI/Inform(R) 1971-2004/Apr 16 (c) 2004 ProQuest Info&Learning	
File 9:	Business & Industry(R) Jul/1994-2004/Apr 15 (c) 2004 The Gale Group	
File 13:	BAMP 2004/Mar W4 (c) 2004 The Gale Group	
File 647:	CMP Computer Fulltext 1988-2004/Apr W1 (c) 2004 CMP Media, LLC	
File 148:	Gale Group Trade & Industry DB 1976-2004/Apr 16 (c) 2004 The Gale Group	

5/3,K/1 (Item 1 from file: 275)
DIALOG(R)File 275:Gale Group Computer DB(TM)
(c) 2004 The Gale Group. All rts. reserv.

01468657 SUPPLIER NUMBER: 10838380 (USE FORMAT 7 OR 9 FOR FULL TEXT)
Add and delete records the right way. (Mac Rubel's power tools) (column)
Rubel, Mac
Data Based Advisor, v9, n6, p38(3)
June, 1991
DOCUMENT TYPE: column ISSN: 0740-5200 LANGUAGE: ENGLISH
RECORD TYPE: FULLTEXT; ABSTRACT
WORD COUNT: 1517 LINE COUNT: 00127

```
... data
  REPLACE & f [underscore] name WITH .f.
  CASE type (f [underscore] name) = 'M' && memo field
    REPLACE & f [underscore] name WITH"
  ENDCASE
  i = i + 1 && increment counter
  ENDDO ENDIF RETURN (rvalue)
  Both DELE [underscore] REC and RBLANK are bare-bones functions, but
...
```


5/3,K/2 (Item 1 from file: 16)
DIALOG(R)File 16:Gale Group PROMT(R)
(c) 2004 The Gale Group. All rts. reserv.

04992154 Supplier Number: 47332404 (USE FORMAT 7 FOR FULLTEXT)
ARM stretches out to Java environment
Jaggar, Dave
Electronic Engineering Times, p082
April 28, 1997
Language: English Record Type: Fulltext
Document Type: Magazine/Journal; Trade
Word Count: 1996

... 1
Again we utilize ARM's ability to perform a memory access and update the **address** -register value in a single instruction. This sequence automatically fetches the byte code and **increments** the program **counter** . A C **switch** statement would now use the byte code as an index into a jump table to find the **address** of the ARM-code sequence corresponding to that byte code, and then jump to that...

5/3,K/6 (Item 1 from file: 647)

DIALOG(R)File 647:CMP Computer Fulltext
(c) 2004 CMP Media, LLC. All rts. reserv.

01124185 CMP ACCESSION NUMBER: EET19970428S0100

ARM stretches out to Java environment

Dave Jaggar, Engineering Manager, Advanced RISC Machines Ltd., Cambridge,
England

ELECTRONIC ENGINEERING TIMES, 1997, n 951, PG82

PUBLICATION DATE: 970428

JOURNAL CODE: EET LANGUAGE: English

RECORD TYPE: Fulltext

SECTION HEADING: Embedded Systems

WORD COUNT: 1989

... 1
Again we utilize ARM's ability to perform a memory access and
update the **address** -register value in a single instruction. This sequence
automatically fetches the byte code and **increments** the program **counter**
. A C **switch** statement would now use the byte code as an index into a
jump table to find the **address** of the ARM-code sequence corresponding
to that byte code, and then jump to that...

Set	Items	Description
S1	7617	(INCREASE? OR INCREMENT? OR RAISE?) (3N) (COUNTER?)
S2	1342	S1 AND (SUBSTITUTE? OR SWITCH? OR APPEND? OR SWAP? OR REPL- ACE?)
S3	255	S2 AND (NAME? OR NAMING OR LABEL? OR ID OR IDENTIF? OR ADD- RESS? OR IDS)
S4	128	S3 AND IC=(G06F? OR H04L?)
S5	25	S3 AND IC=G06F-015?
S6	25	S5 NOT AD=20001208:20021208
S7	25	S6 NOT AD=20021208:20040501
S8	18	S7 AND (TREE OR BTREE OR DEVICE? OR LU OR LUN OR UNIT? ? OR NODE? OR CHILD OR PARENT? OR SLAVE? OR MASTER?)

File 347:JAPIO Nov 1976-2003/Dec(Updated 040402)

(c) 2004 JPO & JAPIO

File 350:Derwent WPIX 1963-2004/UD,UM &UP=200424

(c) 2004 Thomson Derwent

8/5/3 (Item 3 from file: 347)
DIALOG(R)File 347:JAPIO
(c) 2004 JPO & JAPIO. All rts. reserv.

02487286 **Image available**
PICTURE ENLARGING DEVICE

PUB. NO.: 63-104186 [JP 63104186 A]
PUBLISHED: May 09, 1988 (19880509)
INVENTOR(s): KATSURA TAKUJI
APPLICANT(s): MATSUSHITA ELECTRIC IND CO LTD [000582] (A Japanese Company
or Corporation), JP (Japan)
APPL. NO.: 61-251124 [JP 86251124]
FILED: October 22, 1986 (19861022)
INTL CLASS: [4] G06F-015/66
JAPIO CLASS: 45.4 (INFORMATION PROCESSING -- Computer Applications)
JOURNAL: Section: P, Section No. 760, Vol. 12, No. 350, Pg. 76,
September 20, 1988 (19880920)

ABSTRACT

PURPOSE: To enlarge a picture which is approximate to an original picture by permitting a space product sum calculating circuit to interpolate data.

CONSTITUTION: Multigradation picture data (x-lines X y-lines) is stored. Before data is transferred to a buffer memory 3 from an accessible frame memory 1 on a line basis, a CPU 7 creates an **address** conversion table in a look-up table LUT 5. Afterwards an **address counter** 4 is **incremented** or decremented. An **address** is given to the buffer memory 3 through a selector 8 and data is written in the buffer memory 3. Then an input from the selector 8 is **switched** to the LUT 5 from the **address counter** 4, which is **incremented** or decremented. An **address** is given to the buffer memory 3 through the LUT 5 and the selector 8. The space product sum calculating circuit 6 interpolates data outputted from the buffer memory 3. Picture data is written in the frame memory 1 or a display memory 2, and a picture is displayed on a display **device** 9.

8/5/9 (Item 5 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2004 Thomson Derwent. All rts. reserv.

007465475

WPI Acc No: 1988-099409/198815

XRPX Acc No: N88-075336

Structured data memory for universal application - allows locations to be automatically addressed for entry of data

Patent Assignee: OPTROMATION GMBH (OPTR-N)

Inventor: BEUSHAUSEN J; THERBURG R D

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
DE 3633157	A	19880407	DE 3633157	A	19860930	198815 B

Priority Applications (No Type Date): DE 3633157 A 19860930

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
DE 3633157	A	4		

Abstract (Basic): DE 3633157 A

Data from an image sensor or other peripheral **device** are entered into a matrix memory. The main computer generates **addresses** via the **switching** logic to control the entry. A section of **address** is converted and is entered into a microprograinstruction counter. Locations within the matrix memory are **identified** by X and Y **address** registers.

Micro-instructions are sequentially read from a microprogramme memory. An **incrementing counter** provides an **address** for a buffer handling the data output from the matrix memory.

ADVANTAGE - Automatic **addressing** of memory without requiring main computer.

0/1

Title Terms: STRUCTURE; DATA; MEMORY; UNIVERSAL; APPLY; ALLOW; LOCATE; AUTOMATIC; **ADDRESS** ; ENTER; DATA

Derwent Class: T01

International Patent Class (Additional): G06F-009/32; G06F-012/02;

G06F-015/66

File Segment: EPI

8/5/14 (Item 10 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2004 Thomson Derwent. All rts. reserv.

003242269

WPI Acc No: 1982-A2944E/198202

Memory system for electronic calculator - has decoder in each memory circuit for incrementing program counter

Patent Assignee: TEXAS INSTR INC (TEXI)

Inventor: HAMILTON S P; HUNTER A; LIES K A; MCFARLAND H G

Number of Countries: 005 Number of Patents: 008

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
EP 43222	A	19820106	EP 81302817	A	19810623	198202 B
US 4383184	A	19830510	US 80163024	A	19800626	198321
US 4430724	A	19840207	US 80163023	A	19800626	198408
US 4443845	A	19840417	US 80163237	A	19800626	198418
US 4503494	A	19850305	US 80163025	A	19800626	198512
US 4516218	A	19850507				198521
EP 43222	B	19900110				199003
DE 3177144	G	19900215				199008

Priority Applications (No Type Date): US 80163238 A 19800626; US 80163023 A 19800626; US 80163024 A 19800626; US 80163025 A 19800626; US 80163237 A 19800626

Cited Patents: 3.Jnl.Ref; GB 2016754; No-SR.Pub; US 3962683; US 4041461

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
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EP 43222	A	E	113		
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Designated States (Regional): DE FR GB NL

EP 43222	B	E			
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Designated States (Regional): DE FR GB NL

Abstract (Basic): EP 43222 A

The memory system couples memory circuits to controller circuits in a calculator. The controller circuit (620) transmits command, data and **address** signals via a single bus (622) and is responsive to selected data and **address** signals on the bus. The memory circuits (650) selectively transfer data to and from the controller circuit in response to selected command and **address** signals.

A command decode circuit (632) in each memory circuit pref. includes a **device** for **incrementing** the program **counter** (634) after each memory operation in response to selected command signals. This enables a number of sequential memory operations in response to a single command. At least one memory circuit is pref. a plug-in read-write memory module having a power **switching** circuit (648) for providing continuous power from the main power bus (628) of the data processing system or a battery (646) housing within the plug-in memory.

28B

Title Terms: MEMORY; SYSTEM; ELECTRONIC; CALCULATE; DECODE; MEMORY; CIRCUIT ; INCREMENT; PROGRAM; COUNTER

Derwent Class: T01

International Patent Class (Additional): G06F-001/00; G06F-009/30;

G06F-013/00; **G06F-015/02** ; G11C-005/00; H02J-007/00

File Segment: EPI

Set	Items	Description
S1	427788	LU OR LUS OR LUN OR LUNS OR PU OR (PHYSICAL OR LOGICAL) ()U-NIT? ? OR NODE? OR NETWORK()DEVICE? OR SUBNODE?
S2	1638175	CHILD? OR SUBORDINAT? OR BRANCH? OR TWIG? ? OR SUBDIRECTOR?
S3	3620180	NAME? OR NAMING OR LABEL? OR IDENTIF? OR ID OR IDS
S4	3919186	COUNTER? OR NUMBER? OR INCREMENT? OR DECREMENT? OR NUMERAT?
S5	419927	TRUNCAT? OR STEM OR STEMS OR STEMMING OR ABBREVIAT?
S6	463	S1 AND S2 AND S3 AND S4
S7	32	S5 AND S6
S8	12714	S2(2N)S3
S9	115	S1 AND S8
S10	31	S9 AND (S5 OR S4)
S11	31	S10 NOT S7
S12	221	S3 AND S4 AND S5(N) (TERM? OR WORD? OR LABEL? OR ID OR IDENTIFIER? OR NAME?)
S13	3765974	COMBIN? OR JOIN? OR ADD OR APPEND? OR ADDING
S14	88	S1 AND S2 AND (NAMING OR LABELING OR (ASSIGN?) () (NAME? OR - LABEL? OR ID OR IDS OR IDENTIFIER?))
S15	6	S14 AND S5
S16	65	S7 OR S11 OR S15
S17	51	RD (unique items)
S18	40	S17 NOT PY>2000
S19	40	S18 NOT PD=20001208:200211208
S20	40	S19 NOT PD=20021208:20040420
S21	27	S12 AND S13
S22	4	S14 AND S4 AND S5
S23	31	S21 OR S22
S24	24	RD (unique items)
S25	17	S24 NOT PY>2000
S26	14	S25 NOT S18
S27	2	S26 AND (S1 OR COMPUTERI? OR NETWORK? OR DEVICE? OR ADDRESS? OR INTRANET? OR SYSTEM? OR PROCESSOR?)
S28	14	RD S26 (unique items)
S29	14	S28 NOT PY>2000
S30	14	S27 OR S29
File	8: Ei Compendex(R) 1970-2004/Apr W1	(c) 2004 Elsevier Eng. Info. Inc.
File	35: Dissertation Abs Online 1861-2004/Mar	(c) 2004 ProQuest Info&Learning
File	202: Info. Sci. & Tech. Abs. 1966-2004/Feb 27	(c) 2004 EBSCO Publishing
File	65: Inside Conferences 1993-2004/Apr W2	(c) 2004 BLDSC all rts. reserv.
File	2: INSPEC 1969-2004/Apr W1	(c) 2004 Institution of Electrical Engineers
File	94: JICST-EPlus 1985-2004/Mar W4	(c) 2004 Japan Science and Tech Corp(JST)
File	111: TGG Natl. Newspaper Index(SM) 1979-2004/Apr 16	(c) 2004 The Gale Group
File	233: Internet & Personal Comp. Abs. 1981-2003/Sep	(c) 2003 EBSCO Pub.
File	6: NTIS 1964-2004/Apr W3	(c) 2004 NTIS, Intl Cpyrght All Rights Res
File	144: Pascal 1973-2004/Apr W1	(c) 2004 INIST/CNRS
File	434: SciSearch(R) Cited Ref Sci 1974-1989/Dec	(c) 1998 Inst for Sci Info
File	34: SciSearch(R) Cited Ref Sci 1990-2004/Apr W2	(c) 2004 Inst for Sci Info
File	99: Wilson Appl. Sci & Tech Abs 1983-2004/Mar	(c) 2004 The HW Wilson Co.

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S3	3620180	NAME? OR NAMING OR LABEL? OR IDENTIF? OR ID OR IDS
S4	3919186	COUNTER? OR NUMBER? OR INCREMENT? OR DECREMENT? OR NUMERAT?
S5	419927	TRUNCAT? OR STEM OR STEMS OR STEMMING OR ABBREVIAT?
S6	463	S1 AND S2 AND S3 AND S4
S7	32	S5 AND S6
S8	12714	S2(2N)S3
S9	115	S1 AND S8
S10	31	S9 AND (S5 OR S4)
S11	31	S10 NOT S7
S12	221	S3 AND S4 AND S5(N) (TERM? OR WORD? OR LABEL? OR ID OR IDENTIFIER? OR NAME?)
S13	3765974	COMBIN? OR JOIN? OR ADD OR APPEND? OR ADDING
S14	88	S1 AND S2 AND (NAMING OR LABELING OR (ASSIGN?) () (NAME? OR - LABEL? OR ID OR IDS OR IDENTIFIER?))
S15	6	S14 AND S5
S16	65	S7 OR S11 OR S15
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S20	40	S19 NOT PD=20021208:20040420
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File	65: Inside Conferences	1993-2004/Apr W2 (c) 2004 BLDSC all rts. reserv.
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File	144: Pascal	1973-2004/Apr W1 (c) 2004 INIST/CNRS
File	434: SciSearch(R) Cited Ref Sci	1974-1989/Dec (c) 1998 Inst for Sci Info
File	34: SciSearch(R) Cited Ref Sci	1990-2004/Apr W2 (c) 2004 Inst for Sci Info
File	99: Wilson Appl. Sci & Tech Abs	1983-2004/Mar (c) 2004 The HW Wilson Co.

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S2	131	S1 (10N) (SUBSTITUTE? OR SWITCH? OR APPEND? OR SWAP? OR RE-PLACE?)
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File 47:	Gale Group Magazine DB(TM) 1959-2004/Apr 16 (c) 2004 The Gale group	
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File 16:	Gale Group PROMT(R) 1990-2004/Apr 15 (c) 2004 The Gale Group	
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File 141:	Readers Guide 1983-2004/Apr (c) 2004 The HW Wilson Co	
File 696:	DIALOG Telecom. Newsletters 1995-2004/Apr 15 (c) 2004 The Dialog Corp.	
File 553:	Wilson Bus. Abs. FullText 1982-2004/Apr (c) 2004 The HW Wilson Co	
File 621:	Gale Group New Prod. Annou. (R) 1985-2004/Apr 16 (c) 2004 The Gale Group	
File 674:	Computer News Fulltext 1989-2004/Apr W1 (c) 2004 IDG Communications	
File 160:	Gale Group PROMT(R) 1972-1989 (c) 1999 The Gale Group	
File 15:	ABI/Inform(R) 1971-2004/Apr 16 (c) 2004 ProQuest Info&Learning	
File 9:	Business & Industry(R) Jul/1994-2004/Apr 15 (c) 2004 The Gale Group	
File 13:	BAMP 2004/Mar W4 (c) 2004 The Gale Group	
File 647:	CMP Computer Fulltext 1988-2004/Apr W1 (c) 2004 CMP Media, LLC	
File 148:	Gale Group Trade & Industry DB 1976-2004/Apr 16 (c) 2004 The Gale Group	

Set	Items	Description
S1	0	AU=(KAARTIK, V? OR KAARTIK V?)
S2	4	AU=(RATLIFF S? OR RATLIFF, S?)
S3	0	S1 AND S2
S4	3	S2 AND IC=G06F?

File 344:Chinese Patents Abs Aug 1985-2004/Mar
(c) 2004 European Patent Office

File 347:JAPIO Nov 1976-2003/Dec(Updated 040402)
(c) 2004 JPO & JAPIO

File 348:EUROPEAN PATENTS 1978-2004/Apr W01
(c) 2004 European Patent Office

File 349:PCT FULLTEXT 1979-2002/UB=20040408,UT=20040401
(c) 2004 WIPO/Univentio

File 350:Derwent WPIX 1963-2004/UD,UM &UP=200424
(c) 2004 Thomson Derwent

Set	Items	Description
S1	0	AU=(KAARTIK, V? OR KAARTIK V?)
S2	4	AU=(RATLIFF S? OR RATLIFF, S?)
S3	0	S1 AND S2
S4	3	S2 AND IC=G06F?

File 344:Chinese Patents Abs Aug 1985-2004/Mar
(c) 2004 European Patent Office

File 347:JAPIO Nov 1976-2003/Dec(Updated 040402)
(c) 2004 JPO & JAPIO

File 348:EUROPEAN PATENTS 1978-2004/Apr W01
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File 349:PCT FULLTEXT 1979-2002/UB=20040408,UT=20040401
(c) 2004 WIPO/Univentio

File 350:Derwent WPIX 1963-2004/UD,UM &UP=200424
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